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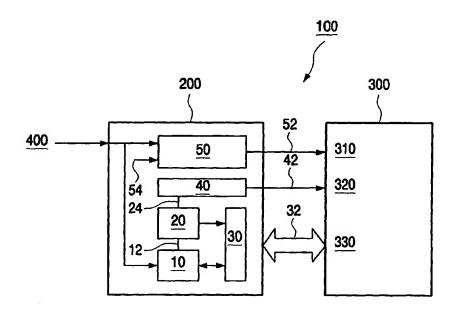
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(54) Title: METHOD AND BASE CHIP FOR MONITORING THE OPERATION OF A MICROCONTROLLER UNIT



(57) Abstract: To further develop a method and a base chip (200) for monitoring the operation of at least one microcontroller that is intended for at least one application and is associated with a system (100) in such a way that a failure in the reset function can be reliably detected and the conclusions that need to be drawn for system-related reasons can be drawn, it is proposed that: the microcontroller unit (300) has at least one monitoring module (10) associated with it and that; the fact that a reset of the microcontroller unit (300) has taken place is acknowledged to the monitoring module (10) by means of at least one confirming signal.



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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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X	US 6 307 480 B1 (SHELDON PETER ET AL) 23 October 2001 (2001-10-23) figure 1 column 1, line 38-62 column 2, line 44 -column 3, line 5 column 4, line 17-60 column 5, line 11-19	1-10
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X Further documents are listed in the continuation of box C.	χ Patent family members are listed in annex.
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